



Description

JMT N-channel Enhancement Mode Power MOSFET

Features

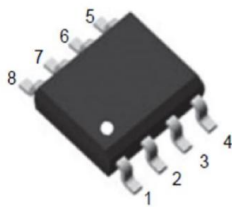
- 30V, 20A
 $R_{DS(ON)} < 6m\Omega @ V_{GS} = 10V$
 $R_{DS(ON)} < 8.6m\Omega @ V_{GS} = 4.5V$
- Advanced Trench Technology
- Provide Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead free product is acquired

Application

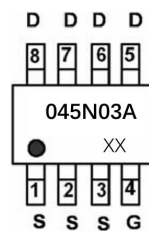
- Load Switch
- PWM Application
- Power management



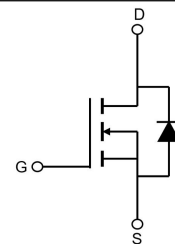
100% UIS TESTED!
100% ΔVds TESTED!



SOP-8 TOP View



Marking and pin Assignment



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	OUTLINE	Device Package	Reel Size	Reel (PCS)	Per Carton (PCS)
045N03A	JMTP045N03A	TAPING	SOP-8	13inch	4000	48000

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Max.	Units
V_{DSS}	Drain-Source Voltage	30	V
V_{GSS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	$T_A = 25^\circ\text{C}$	20
		$T_A = 100^\circ\text{C}$	13
I_{DM}	Pulsed Drain Current ^{note1}	80	A
E_{AS}	Single Pulsed Avalanche Energy ^{note2}	100	mJ
P_D	Power Dissipation	4	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	31.3	$^\circ\text{C}/\text{W}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150	$^\circ\text{C}$



Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} =0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} =±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	0.7	1.0	1.5	V
R _{DS(on)}	Static Drain-Source on-Resistance <small>note3</small>	V _{GS} =10V, I _D =20A	-	4.6	6	mΩ
		V _{GS} =4.5V, I _D =10A	-	6.1	8.6	
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1.0MHz	1214	1700	2295	pF
C _{oss}	Output Capacitance		229	320	432	pF
C _{rss}	Reverse Transfer Capacitance		214	300	405	pF
Q _g	Total Gate Charge	V _{DS} =15V, I _D =10A, V _{GS} =10V	32	45	60.8	nC
Q _{gs}	Gate-Source Charge		-	3	-	nC
Q _{gd}	Gate-Drain("Miller") Charge		11	15	20	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} =15V, I _D =20A, R _{GEN} =3Ω, V _{GS} =10V	-	21	-	ns
t _r	Turn-on Rise Time		-	32	-	ns
t _{d(off)}	Turn-off Delay Time		-	59	-	ns
t _f	Turn-off Fall Time		-	34	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	20	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	80	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =20A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I _F =20A, di/dt=100A/μs	-	15	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge		-	4	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition: T_J=25°C, V_{GS}=15V, R_G=25Ω, L=0.5mH, I_{AS}=20A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%



Typical Performance Characteristics

Figure 1: Output Characteristics

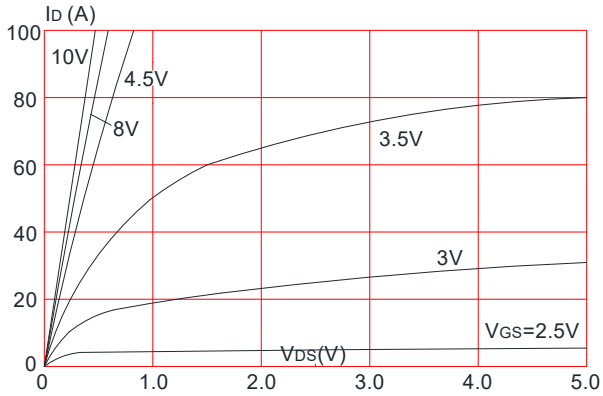


Figure 2: Typical Transfer Characteristics

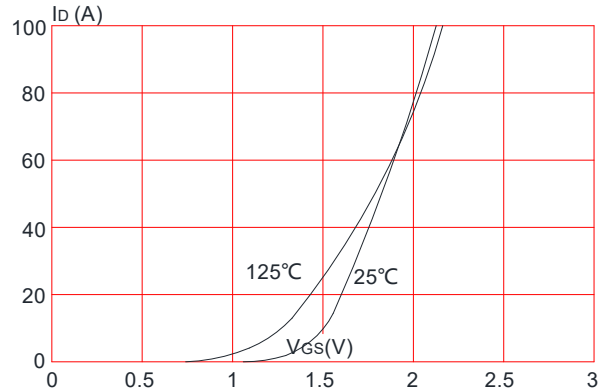


Figure 3: On-resistance vs. Drain Current

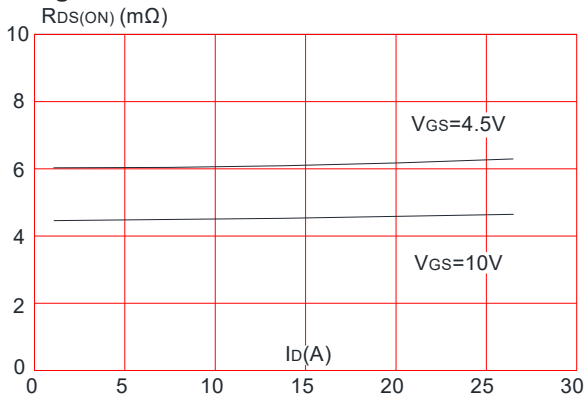


Figure 4: Body Diode Characteristics

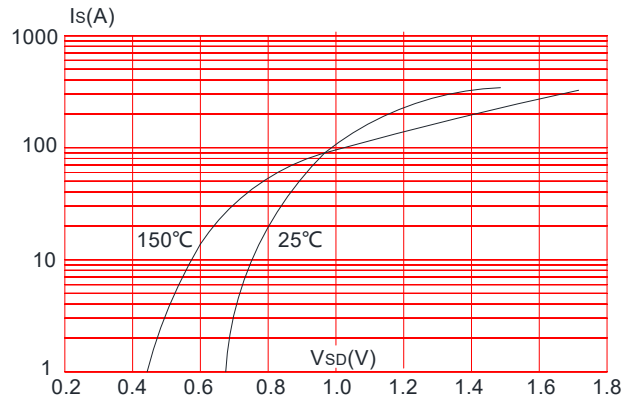


Figure 5: Gate Charge Characteristics

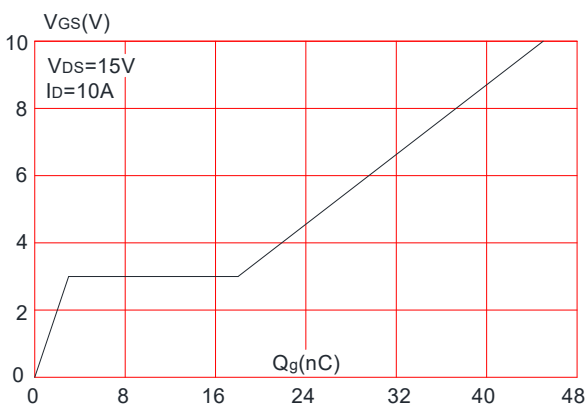


Figure 6: Capacitance Characteristics

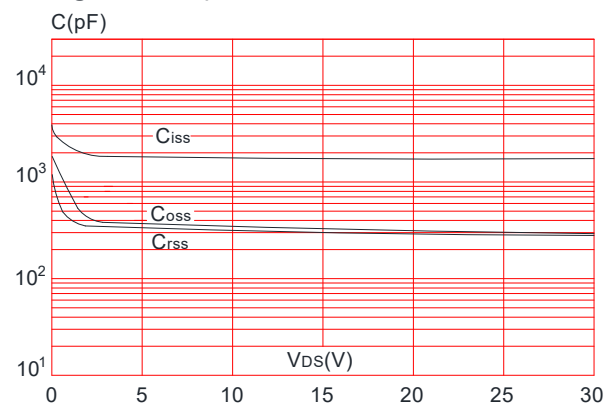




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

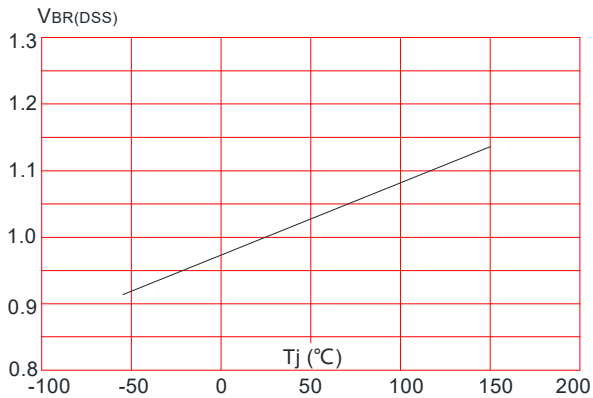


Figure 8: Normalized on Resistance vs. Junction Temperature

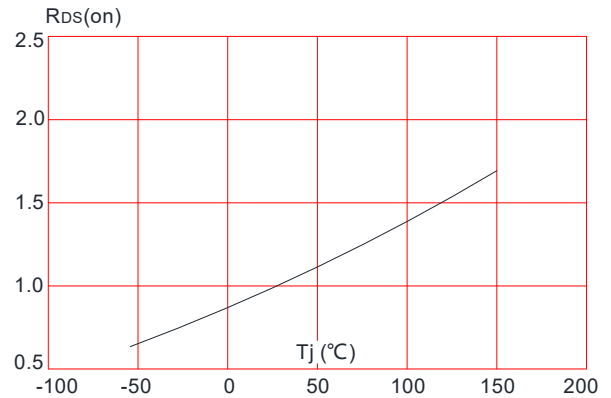


Figure 9: Maximum Safe Operating Area

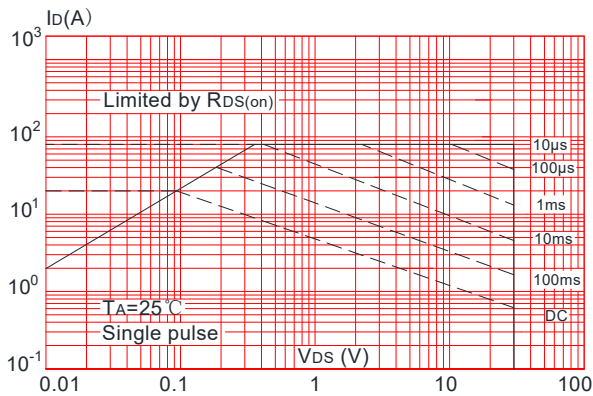


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature

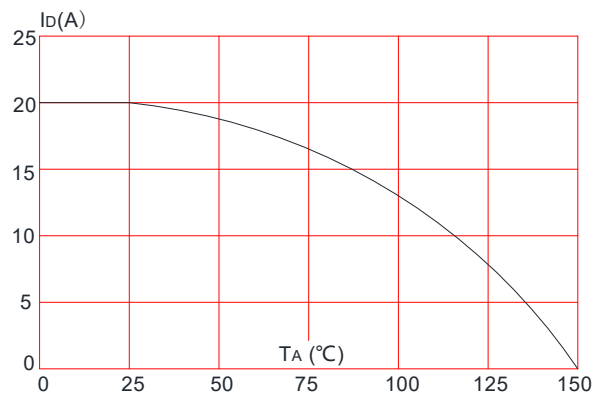
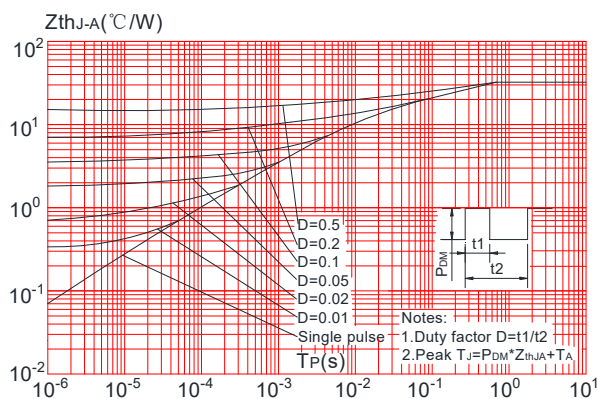


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient



Test Circuit

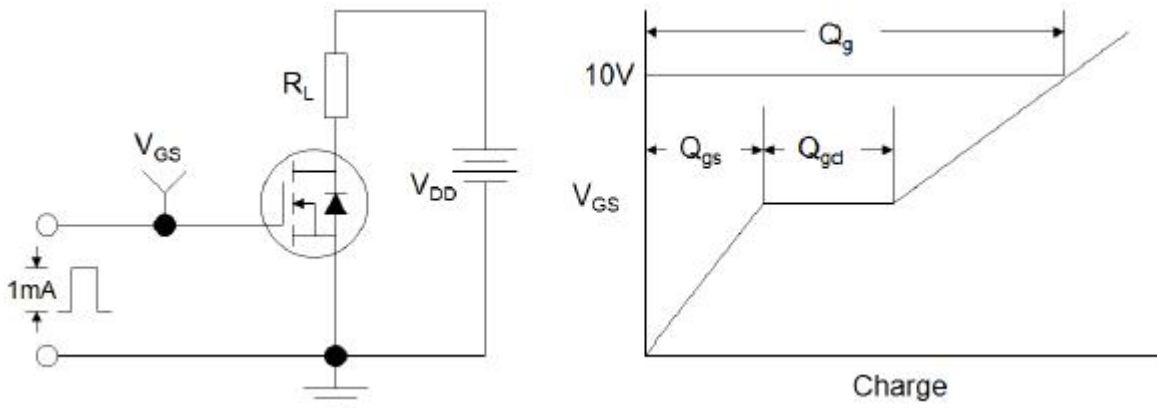


Figure1:Gate Charge Test Circuit & Waveform

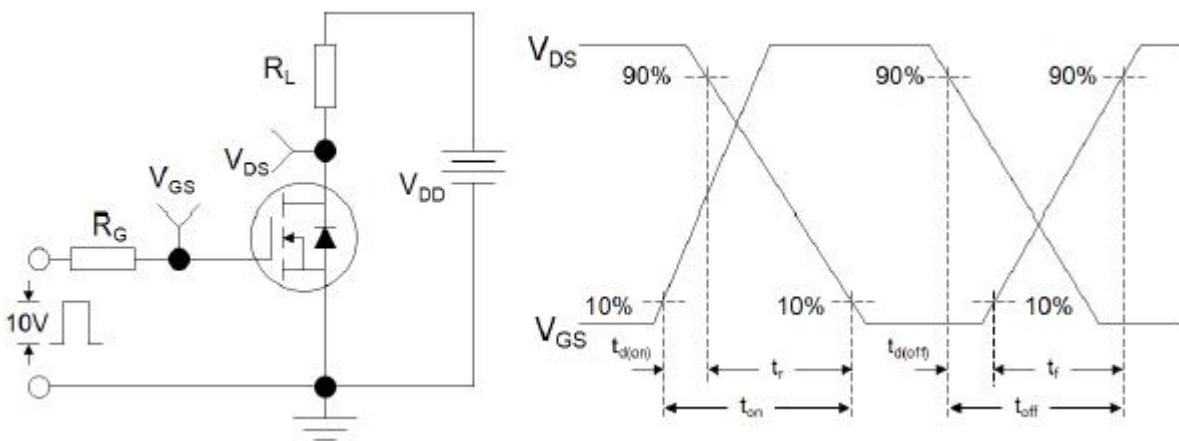


Figure 2: Resistive Switching Test Circuit & Waveforms

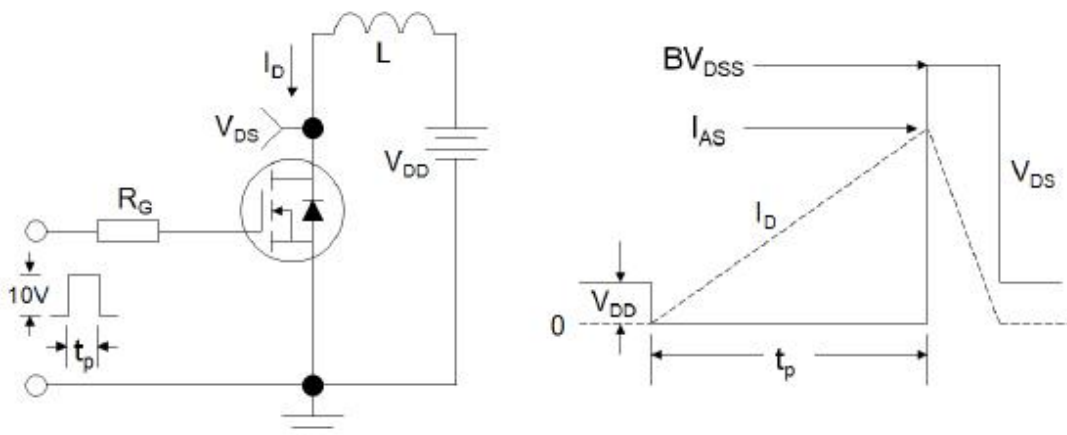
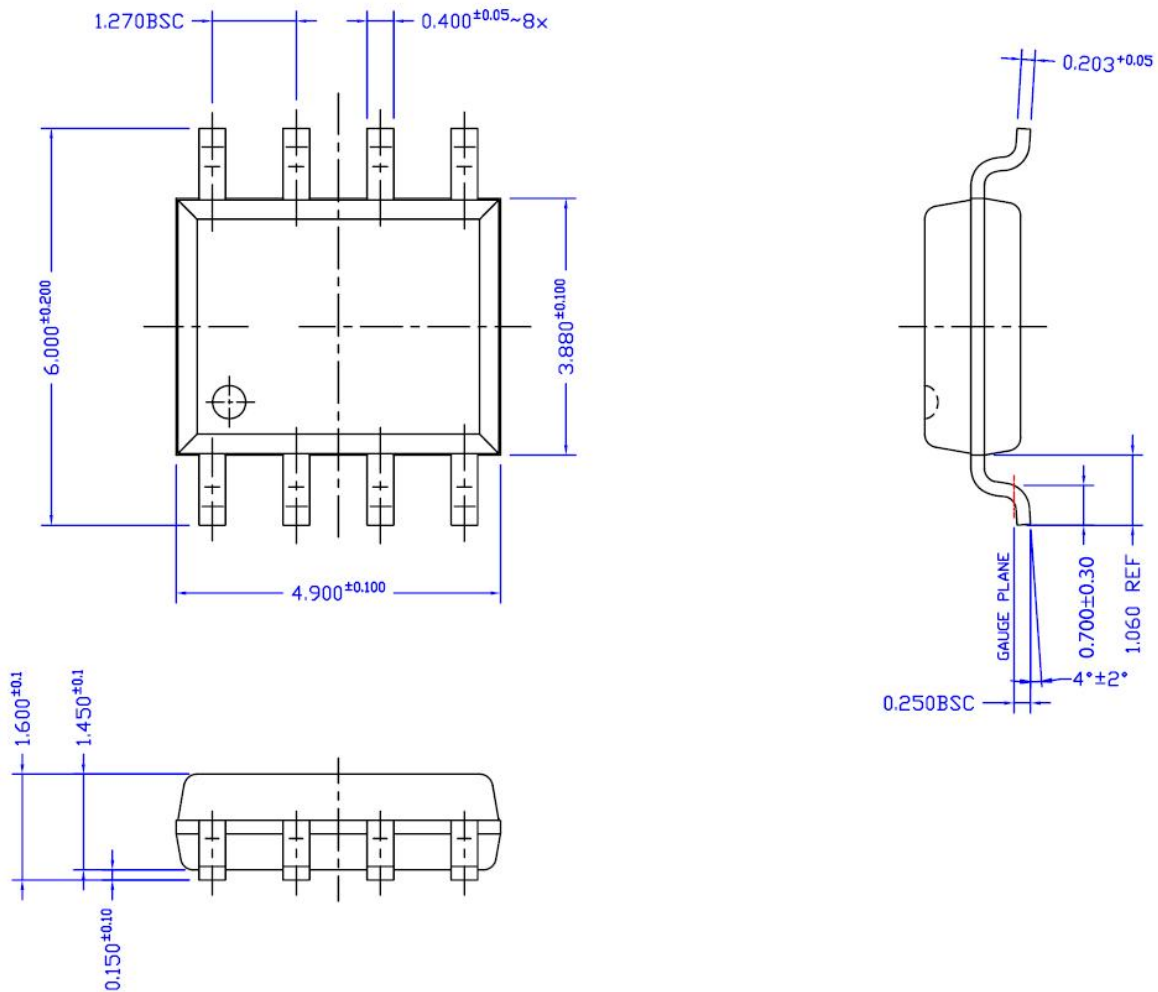


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms



Package Mechanical Data-SOP-8



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